Analysis of A New Kind of Quadruple Folded Substrate Integrated Waveguide Filter with LTCC Technology by Wave Concept Iterative Process

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Abstract—The article presents an efficient method named wave concept iterative process(WCIP) for characterization of substrate integrated waveguide(SIW) structures in LTCC.Firstly, an extensible approach of the iterative method to study substrates with \( n \) layers is researched. The approach involves the mixed magnetic and electric filed equation technique and the multilayer contribution of wave concept iterative process, which involves \( S \)-parameters extraction technique based on a simple form of Matched Load Simulation, and then, Substrate integrated circuits are considered as an ensemble of conducting vias placed in a parallel-plate waveguide. Finally, A new kind of multilayer SIW filter in LTCC is analyzed using the iterative method. Numerical results obtained are compared with the HFSS results. A good agreement is achieved together with significant improvements both in computational time and memory requirements.

I. INTRODUCTION

With the development of wireless and mobile communications, more and more high-performance RF/microwave bandpass filters are required. Substrate integrated waveguide(SIW) was developed to waveguide technology. Using the periodic via structure SIW can realize planar passive component listed above in arbitrary dielectric. Although SIW filters are already much smaller and lighter as compared to those implemented with traditional waveguides, they are quite larger than conventional microstrip/stripline coupled resonator filters. However, the application of LTCC technology makes the realization of multilayered high-performance SIW filters possible[1], owing to its three-dimensional integration capabilities. Taking advantages of the multilayer nature of LTCC technology, SIW is stacked in vertical dimension[2-4]. Circuit area therefore can be significantly reduced.

The analysis of substrate integrated structures and multilayer microstrip circuits has been carried out in many ways. The problem of scattering by an array of cylindrical plates was investigated using 3D methods. However, the analysis becomes more complicated when the number of the cylinders and layers increase. The problem can be simplified by considering unit cell in periodic array case, but in the case of an array with arbitrary arrangement the choice of unit periodic cell is impossible. When using the method of moments (MoM)[5], the computation of the boring Green’s functions[6-8] for multilayer media are inevitable, and the slowly convergent basis functions lead to a large calculation time. For the finite elements method (FEM)[9], a great number of cells are needed to simulate the whole spatial structures and amount of memory are taken up.

In this paper, we present a novel quadruple folded SIW filter. The proposed configuration is analyzed by a new method based on the Multilayer Contribution of Wave Concept Iterative Process (MLC-WCIP)[10]. The MLC-WCIP method are developed and adapted in but to resolve this type of structure. In addition, Substrate integrated circuits are considered as an ensemble of conducting vias placed in a parallel-plate waveguide. The approach is based on the wave concept formulation and the iterative resolution of two relationships between incident and reflected volume-waves[11]. The reflection operator is expressed using Hankel functions and computed by considering the scattering from the ensemble of conducting posts. The simulation results are validated with those calculated with HFSS commercial code.

II. FORMULATION

A. The Operator of Reflection Based on Multilayer Substrates

The WCIP first represents the boundary condition on the upper and lower interface of circuit in term of waves[12], and then sets up relationship of the incident waves and scattered waves both in spatial and spectral domain (Figure 1). We define the incident and reflected wave in both sides of the discontinuity plane \( \forall i = 1, 2 \)

\[
A_i = \frac{1}{2\sqrt{Z_{ii}}} (E + Z_{ii}J) \quad B_i = \frac{1}{2\sqrt{Z_{ii}}} (E - Z_{ii}J)
\]

where:

\[
Z_{ii} = \frac{\mu_0}{\sqrt{\varepsilon_{r} \varepsilon_{ii}}} \quad \forall i = 1, 2
\]

is the medium impedance. A and B are, respectively, the incident and the reflected waves.

![Spectral Domain](image)

**Figure 1.** Definition of wave concept.

On the interface, there are dielectric, metal, and source domains. It is necessary to determine the scattering matrix on each region, so we can calculate the scattering matrix in spatial domain. It takes the following expression[10]:

\[
S_{ii} = \begin{pmatrix}
-H_{-1+N} & -H_{-1+N} & 2H_{-1+N} & 2H_{-1+N} \\
1 + N & 1 + N & 1 + N & 1 + N \\
-H_{1+N} & -H_{1+N} & 2H_{1+N} & 2H_{1+N} \\
1 + N & 1 + N & 1 + N & 1 + N
\end{pmatrix}
\]

(3)

Where
\[ N = \sqrt{\frac{Z_{01}}{Z_{02}}}, n_1 = \frac{Z_0}{Z_{01}}, n_2 = \frac{Z_0}{Z_{02}}, m = \frac{Z_0}{\sqrt{Z_{01}Z_{02}}} . \]

And \( H_j = 1 \) on the domain \( j \), \( H_j = 0 \) elsewhere, where \( j = \text{source} \), dielectric, or metal domain.

In spatial domain, the relationship of the incident and scattered waves is:

\[
\begin{bmatrix}
B_1 \\
B_2
\end{bmatrix} = \begin{bmatrix}
1 & \gamma
\end{bmatrix} \begin{bmatrix}
A_1 \\
A_2
\end{bmatrix} + \begin{bmatrix}
\frac{E_0}{\sqrt{Z_{01}}} H_y \\
\frac{E_0}{\sqrt{Z_{01}}} H_z
\end{bmatrix}
\]

In spectral domain, the relationship between waves is as follows:

\[
\begin{bmatrix}
B_{i,TE} \\
B_{i,TM}
\end{bmatrix} = \begin{bmatrix}
\Gamma_{i,TE} & 0 \\
0 & \Gamma_{i,TM}
\end{bmatrix} \begin{bmatrix}
A_{i,TE} \\
A_{i,TM}
\end{bmatrix}
\]

Where

\[
\Gamma_{i,\alpha} = \begin{vmatrix}
1 - Z_{0,\alpha} \gamma_{mn,\alpha} & \cot \gamma \gamma_{mn,\alpha} \\
1 + Z_{0,\alpha} \gamma_{mn,\alpha} & \cot \gamma \gamma_{mn,\alpha}
\end{vmatrix} = \begin{vmatrix}
\gamma_{mn,TE} & \frac{\gamma_{mn,TE}}{j \omega \varepsilon_0 \mu_0} \\
\gamma_{mn,TM} & \frac{\gamma_{mn,TM}}{j \omega \varepsilon_0 \mu_0}
\end{vmatrix}
\]

\[
\gamma_{mn,\alpha}^2 = \left( \frac{m \pi}{a} \right)^2 + \left( \frac{n \pi}{b} \right)^2 - k_0^2 \varepsilon_{ri}
\]

\[ k_0 \] is the space number. For each iteration, we combine the wave concept with the 2D-FFT algorithm to change the type of domain. This technique is called Fast Mode Transformation (FMT). This procedure contains Fourier transform and Mode transform. The iterative procedure mechanism is summarized in Figure 2.

Figure 2. Schematic description of the iterative process.

For the multilayer structure (Figure 3), the relation of waves between the layers in the spectral domain should be taken into account. Accordingly, the transmission line theory is used to express this relation[13]. the scattering matrix of the transmission line is given by

\[
S = \frac{1}{\Delta} \begin{pmatrix}
(Z_0^2 - Z_{0,j}) s(h_{j+1}) & 2Z_0 \sqrt{Z_{0,j}Z_{0,j+1}} \\
2Z_0 \sqrt{Z_{0,j}Z_{0,j+1}} & (Z_0^2 - Z_{0,j+1}) s(h_{j+1})
\end{pmatrix}
\]

With

\[
\Delta = 2Z_0 \sqrt{Z_{0,j}Z_{0,j+1}} s(h_{j+1}) + (Z_0^2 - Z_{0,j}Z_{0,j+1}) s(h_{j+1})
\]

Where \( Z_0 \) is the transmission line impedance, \( Z_{0,j} \) is the characteristic impedance of the layer \( j \), \( \gamma \) is the propagation constant in the line.

Figure 3 (a) Multilayered structure and (b) its equivalent circuit

B. The Operator of Reflection Based on Substrate Integrated Waveguide

We consider the case of cylindrical wires placed between two parallel metallic plates in substrate integrated waveguide such as illustrated in Fig. 4.

Figure 4 Cylindrical wires between two parallel metallic plates

The current density \( J \) has only \( z \) component \( (J_z) \), but it is dependent of \( z \). he electric field can be given by the following[11]:

\[
E = -\nabla \phi - \frac{\partial}{\partial t} F = \frac{1}{j \omega \varepsilon_0 \mu_0} \nabla (\nabla \dot{F}) - j \omega F
\]

In the Eq.(8), the potential vector \( F \) has only \( z \) component. With the presence of the parallel plates, we can consider that all electromagnetic fields are with \( e^{j\omega t} \). By mathematical manipulation applied to the equation of Maxwell-Faraday Eq.(8), we obtain the following:

\[
(\Delta_T + k^2)F = -u_0 J
\]

where \( \Delta_T \) is the transverse Laplace operator. Using the equation of Maxwell-Faraday with \( \nabla (\nabla \dot{F}) = -\alpha^2 F \), we obtain the following:

\[
E = -j \omega (1 - \alpha^2 k^2)\dot{F}
\]

To have relationship between the electric field and the
current density, we combine the Eqs.(9) and (10):

\[
(\Delta_T + k^2)E = -j\omega(1 - \frac{\alpha^2}{k^2})uJ
\]  

(11)

The expression (10) can be expressed as follows:

\[
\dot{J} = \hat{Y}E
\]  

(12)

Where \( \hat{Y} = \frac{1}{j\omega(1 - \frac{\alpha^2}{k^2})} \) is the admittance operator. The relationship between incident and reflected waves is given by the following:

\[
A = \hat{\Gamma}B
\]  

(13)

where \( \hat{\Gamma} \) is the reflection operator which is defined as follows:

\[
\hat{\Gamma} = \frac{Y_0 - \hat{Y}}{Y_0 + \hat{Y}}
\]  

(14)

where \( Y_0 = 1/Z_0 = \omega\epsilon \)

Using the Eqs.(10), (11), (12), and (14), we can obtain the expression of the reflection operator:

\[
\hat{\Gamma} = -1 + 2Y_0j\omega(1 - \frac{\alpha^2}{k^2})[\Delta_T + k^2]^{-1}
\]  

(15)

where \( k_\perp^2 = k^2 + jY_0\omega\mu(1 - \frac{\alpha^2}{k^2}) \)

The solution of the operator \([\Delta_T + k^2]^{-1}\) is given by the expression \( \frac{j}{4}\int H_0^2(k|r - r'|)d^2r' \), where \( H_0^2 \) stands for the second kind Hankel function of order zero. Thus, Eq. (13) can be rewritten as follows:

\[
\hat{\Gamma}B = B + \frac{Y_0\omega\mu}{2}(1 - \frac{\alpha^2}{k^2})\int H_0^2(k_\perp r - r')Bd^2r'
\]  

(16)

### III. DESIGN OF LTCC FILTER BASED ON SUBSTRATE INTEGRATED WAVEGUIDE

To demonstrate the effectiveness of the method, we consider a design of LTCC filter based on quadruple folded substrate integrated waveguide. The structure of the filter is is able to reduce the circuit size by about 85% compared with the conventional substrate integrated waveguide (SIW) resonant cavity. Fig. 5 shows the proposed quadruple folded substrate integrated waveguide resonator.

As well known, substrate integrated waveguide filters usually have better performance than other planar structures. However, their use may be restricted because of the relative large physical dimension. To overcome such a drawback, some miniaturized resonant cavity structures were proposed and investigated. For example, a double-folded SIW resonant cavity was introduced in 2004 [14]. It is a two-layer waveguide resonant cavity with L-type slot in the middle conductor layer, thus it can reduce the circuit area by 75% compared with the conventional SIW resonant cavity. Some researchers made efforts to reduce the size of SIW resonant cavity as well [15-16]. Nevertheless, there is still room for improvement. Filters designed with SIW waveguides and LTCC can enhance the possibility of hybridizing laminated waveguides and planar circuits with significant reduction in circuit size [17-20].

In this paper, the filter is implemented with quadruple folded SIW (QFSIW) cavity. Resonators in LTCC. Conductor-backed sandwiched CPW is employed as the feeding structure to transmit power from microstrip line into the QFSIW resonant cavity. After that, a two-cavity filter is developed and fabricated based on the proposed miniaturized SIW resonant cavity. The filter is implemented on a 8-layer Ferro A6M LTCC substrate with relative permittivity of 5.9 and loss tangent of 0.002, and a uniform dielectric layer thickness of 0.096 mm. All metallic vias have the same diameter of 0.45 mm. Every SIW cavity is built based on a 8-layer substrate. Fig. 6 shows the proposed filter structure. The other size parameters about the two-pole filter as follows: \( L_1 = 9.15\) mm, \( L_2 = 5.65\) mm, \( L_3 = 2.25\) mm, \( W = 1.06\) mm, \( h = 0.768\) mm, \( s = 0.25\) mm, Its overall size is \( 9.15*9.15*0.768\) mm. The presented formulation was implemented in FORTRAN code. The scattering parameters \( S_{11} \) and \( S_{12} \) are shown in Fig. 7. The center frequency is 9.05 GHz, the filter has the fractional bandwidth of 11%. A good agreement between our result and the HFSS result. The computations of the structure were performed on Core(TM) i5 CPU 2.67GHz, 2 GB RAM. In Table 1, CPU time for the case considered in this article are reported. Note that the construction of the matrix \( \Gamma \) is independent of the iteration, thus, it can be done once in the preprocessing step.
IV. CONCLUSIONS

In this paper, we expand the use of WCIP, which is widely used for planar circuit, to analysis the SIW multi-layer circuit—a two cavities LTCC quadraple folded SIW filter. The efficiency of the method has been shown through the analysis of test case of multi-layer SIW structures, with short computational time. A good agreement is achieved between our result and the HFSS result. This new approach is found to be efficient in producing accurate results with good saving in the computer memory usage and computational time and has the capacity to simulate very large structure with large number of via holes.

Figure 7 The proposed filter results

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